

2023-07-28

- Synthesizer Firmware Version History

**Applies to iMS4 rev-D onwards.**

**Earlier revisions are not field programmable.**

**FW v4-1-131**

SDIO is now pipelined equivalently to RF image data so that synchronous output does not corrupt at image clock rates <2.5MHz.

Software reset (using ConfigureNHF) now works correctly: DDS is correctly re-initialised.

**FW v4-1-129**

Firmware update to provide a programmable delay between the RF output pairs and the Image clock.

Ch1 delay = Ch2 delay. Ch3 delay = Ch4 delay

**FW v4-1-128**

Firmware update to resolve interlock enable issue.

Applicable to RF amplifiers controlled via iMS4 rear panel connector J5.

Previous firmware(s) required the DC supply to the RF amplifier be applied prior to the iMS4 power-on.